



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Applicant:** Hiroyuki Nakajima      **Examiner:** Eddie P. Chan  
**Serial No.:** 10/785,671      **Art Unit:** 2183  
**Filed:** February 24, 2004      **Docket:** 17472  
**For:** PROCESSOR, SYSTEM LSI  
CIRCUIT, METHOD OF DESIGNING  
SAME, AND RECORDING MEDIUM  
HAVING SAID METHOD  
RECORDED THEREON      **Dated:** November 15, 2006

**Confirmation No.:** 2687

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 23313-1450

**RESPONSE TO REQUIREMENT FOR RESTRICTION  
AND AMENDMENT**

Sir:

Pursuant to the Restriction Requirement imposed in the Official Action dated September 21, 2006, Applicant provisionally elects without traverse the claims of Group I, i.e., Claims 1-12, and 21-37, for continued prosecution herein. Further Applicant respectfully requests the Examiner to consider the following amendments and remarks prior to examination.

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**CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8(a)**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Dated: November 15, 2006



Katherine R. Vieyra